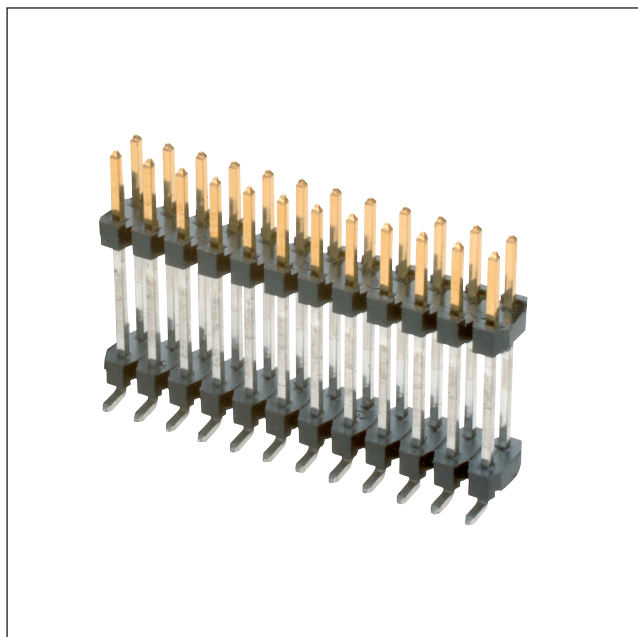


3M™ Pin Strip Header

2 mm x 2 mm Dual Insulator, Straight, Surface Mount

951 Series



- High temperature dielectric
- “No lead” reflow solder compatible
- Optimizes use of PC board space
- End stackable feature
- Mates with boardmount products
- See the Regulatory Information Appendix (RIA) in the “RoHS compliance” section of www.3M.com/interconnects for compliance information

Date Modified: April 24, 2012

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Sheet 1 of 2

Physical

Insulation

Material: High Temperature Thermoplastic
Flammability: UL 94V-0
Color: Black

Contact

Material: Copper Alloy

Plating

Underplating: 1.25–2.5 μm [100 μ''] Nickel
Wiping Area: 0.25 μm [10 μ''] Gold
Solder Tail Area: 1–3 μm [40–118 μ''] Matte Tin

Electrical

Current Rating: 1 A
Insulation Resistance: $> 5 \times 10^8$ Ohm at 100 V_{DC}
Withstanding Voltage: 1000 V_{DC}
Transition Resistance: ≤ 20 mOhm

Environmental

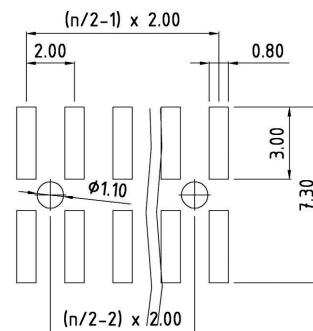
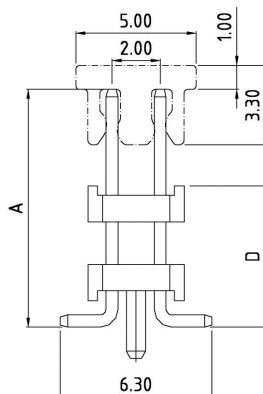
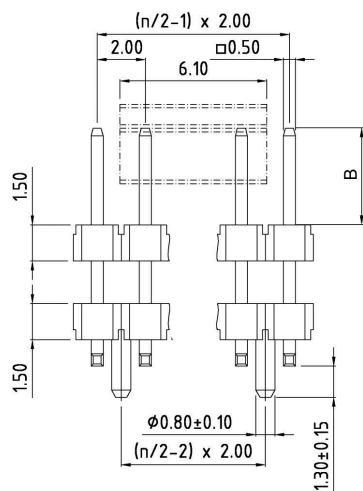
Temperature Range: -25 °C to +105 °C
Processing Temperature: max. 260 °C (Profile per J-STD-020C)
Moisture Sensitivity Level: 1 (per J-STD-020C)

UL File No.: E68080

3M™ Pin Strip Header

2 mm x 2 mm Dual Insulator, Straight, Surface Mount

951 Series



PCB-LAYOUT

Contact Dimensions			
Coding No.	A	Mating Length B	D
13	9.9	4.0	5.9
14	10.7	4.0	6.7
16	11.9	4.0	7.9
17	12.4	4.0	8.4
20	15.4	4.0	11.4
23	17.4	4.0	13.4
Others on Request			

Contact Dimensions			
Coding No.	A	Mating Length B	D
13	11.8	5.9	5.9
14	12.6	5.9	6.7
16	13.8	5.9	7.9
17	14.3	5.9	8.4
20	17.3	5.9	11.4
23	19.3	5.9	13.4
Others on Request			

n = No. of Contacts

Tolerance Unless Noted [mm]				
Nominal Size	0.5-3	3-6	6-30	30-120
Linear Dim	+/- 0.1	+/- 0.1	+/- 0.2	+/- 0.3
Radii	+/- 0.2	+/- 0.5	+/- 1	+/- 1

Ordering Information

9514XX — XXX XX 09 — AR — XX

Contact Quantity:
04-80 Pins / 2 Row

Mounting Style:
2 = Surface Mount,
without Loc. Posts, 2 Row
4 = Surface Mount,
with Loc. Posts, 2 Row
(8-16 Contacts / others on Request)

Mating Pin Length
Dimension B:
01 = 4.0 mm
06 = 5.9 mm

Packaging:
TP = Tube
PT = Tube with Pad

Plating:
AR = Selective Au 0.25 µm / Sn
(RIA E1 & C1 apply)

Board Stacking Height (D):
Coding Number: see Table

Note: Pad options 8 to 80 pos.

TS-2159-C
Sheet 2 of 2

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3M Electronics Solutions Division

6801 River Place Blvd.
Austin, TX 78726-9000
U.S.A.
1-800-225-5373
www.3m.com/interconnects

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