

GM9452

N-CHANNEL ENHANCEMENT MODE POWER MOSFET

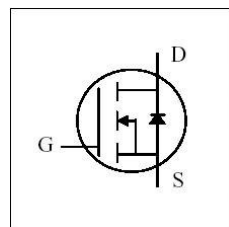
BV _{DSS}	20V
R _{DS(ON)}	50mΩ
I _D	4A

Description

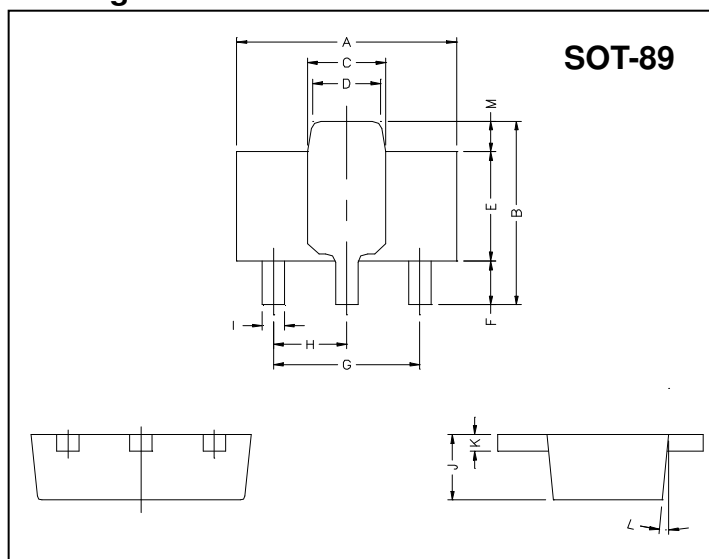
The GM9452 provide the designer with the best combination of fast switching, ruggedized device design, low on-resistance and cost-effectiveness.

Features

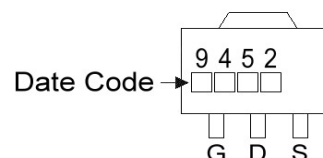
- *Simple Drive Requirement
- *Low Gate Charge
- *Capable of 2.5V Gate Drive



Package Dimensions



Marking :



REF.	Millimeter		REF.	Millimeter	
	Min.	Max.		Min.	Max.
A	4.4	4.6	G	3.00	REF.
B	4.05	4.25	H	1.50	REF.
C	1.50	1.70	I	0.40	0.52
D	1.30	1.50	J	1.40	1.60
E	2.40	2.60	K	0.35	0.41
F	0.89	1.20	L	5°	TYP.
			M	0.70	REF.

Absolute Maximum Ratings

Parameter	Symbol	Ratings	Unit
Drain-Source Voltage	V _{DS}	20	V
Gate-Source Voltage	V _{GS}	±16	V
Continuous Drain Current ³ , V _{GS} @4.5V	I _D @TA=25°C	4	A
Continuous Drain Current ³ , V _{GS} @4.5V	I _D @TA=70°C	2.5	A
Pulsed Drain Current ¹	I _{DM}	12	A
Total Power Dissipation	P _D @TA=25°C	1.25	W
Linear Derating Factor		0.01	W/°C
Operating Junction and Storage Temperature Range	T _j , T _{stg}	-55 ~ +150	°C

Thermal Data

Parameter	Symbol	Value	Unit
Thermal Resistance Junction-ambient ³ Max.	R _{thj-a}	100	°C/W

Electrical Characteristics(T_j = 25°C Unless otherwise specified)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Drain-Source Breakdown Voltage	BV _{DSS}	20	-	-	V	V _{GS} =0, I _D =250uA
Breakdown Voltage Temperature Coefficient	$\Delta BV_{DSS} / \Delta T_j$	-	0.03	-	V/°C	Reference to 25°C, I _D =1mA
Gate Threshold Voltage	V _{GS(th)}	0.7	-	1.5	V	V _{DS} =V _{GS} , I _D =250uA
Forward Transconductance ²	g _{fs}	-	10	-	S	V _{DS} =5V, I _D =3A
Gate-Source Leakage Current	I _{GSS}	-	-	±100	nA	V _{GS} = ±16V
Drain-Source Leakage Current(T _j =25°C)	I _{DSS}	-	-	1	uA	V _{DS} =20V, V _{GS} =0
Drain-Source Leakage Current(T _j =70°C)		-	-	25	uA	V _{DS} =16V, V _{GS} =0
Static Drain-Source On-Resistance ²	R _{DS(ON)}	-	-	38	mΩ	V _{GS} =10V, I _D =4A
		-	-	50		V _{GS} =4.5V, I _D =4A
Total Gate Charge ²	Q _g	-	6	10	nC	I _D =4A V _{DS} =16V V _{GS} =4.5V
Gate-Source Charge	Q _{gs}	-	1	-		
Gate-Drain ("Miller") Charge	Q _{gd}	-	2	-		
Turn-on Delay Time ²	T _{d(on)}	-	8	-	ns	V _{DS} =10V I _D =1A V _{GS} =5V R _G =3.3Ω R _D =10Ω
Rise Time	T _r	-	9	-		
Turn-off Delay Time	T _{d(off)}	-	13	-		
Fall Time	T _f	-	3	-		
Input Capacitance	C _{iss}	-	360	570	pF	V _{GS} =0V V _{DS} =20V f=1.0MHz
Output Capacitance	C _{oss}	-	80	-		
Reverse Transfer Capacitance	C _{rss}	-	65	-		

Source-Drain Diode

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Forward On Voltage ²	V _{SD}	-	-	1.3	V	I _S =1A, V _{GS} =0V
Reverse Recovery Time ²	T _{rr}	-	18	-	ns	I _S =4A, V _{GS} =0V dI/dt=100A/μs
Reverse Recovery Charge	Q _{rr}	-	10	-	nC	

Notes: 1. Pulse width limited by safe operating area.

2. Pulse width ≤ 300us, duty cycle ≤ 2%.

3. Surface mounted on FR4 board, t ≤ 10sec.

Characteristics Curve

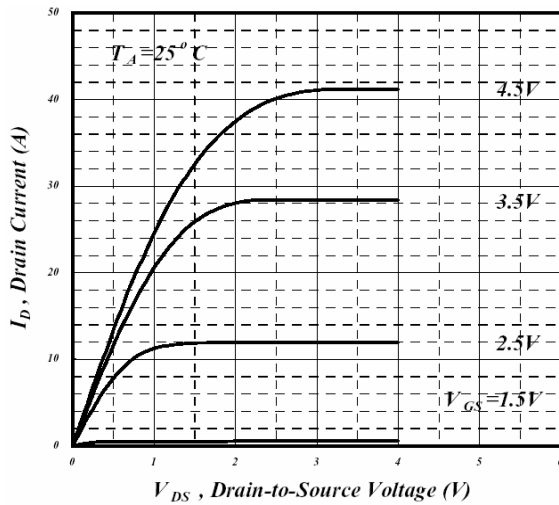


Fig 1. Typical Output Characteristics

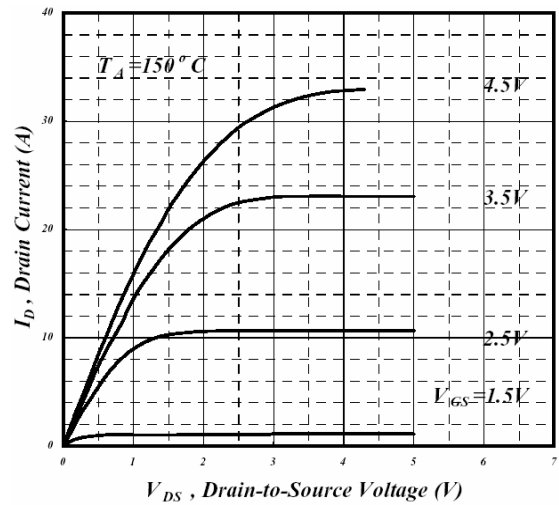


Fig 2. Typical Output Characteristics

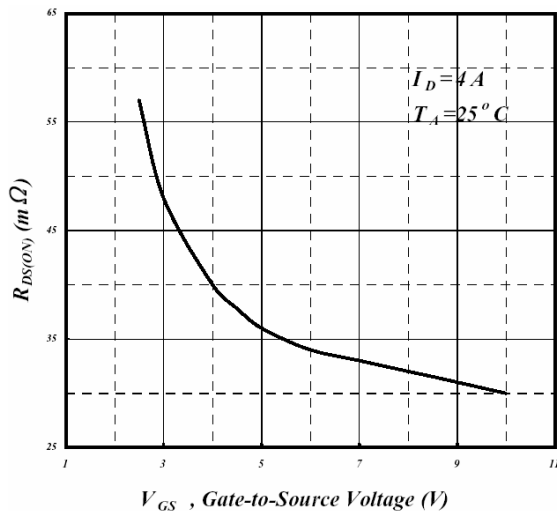


Fig 3. On-Resistance v.s. Gate Voltage

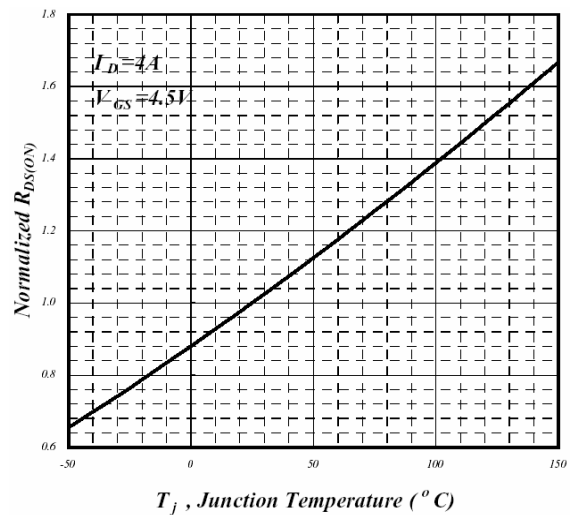


Fig 4. Normalized On-Resistance v.s. Junction Temperature

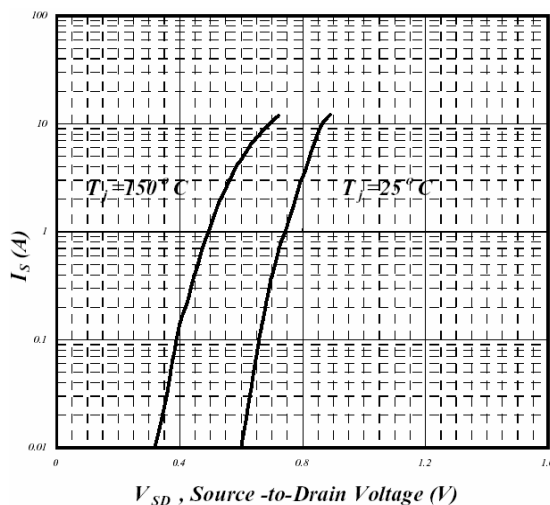


Fig 5. Forward Characteristics of Reverse Diode

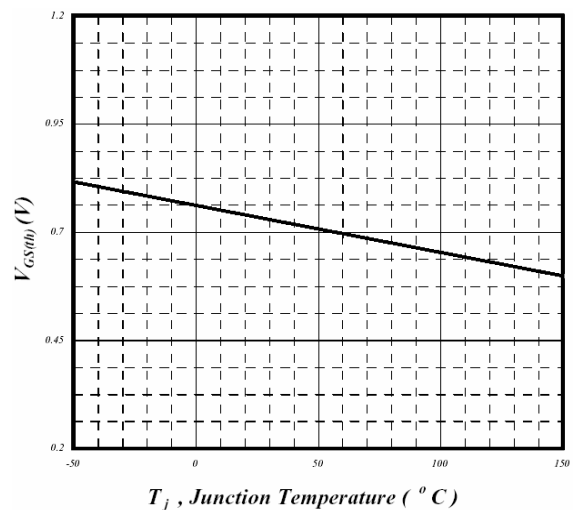


Fig 6. Gate Threshold Voltage v.s. Junction Temperature

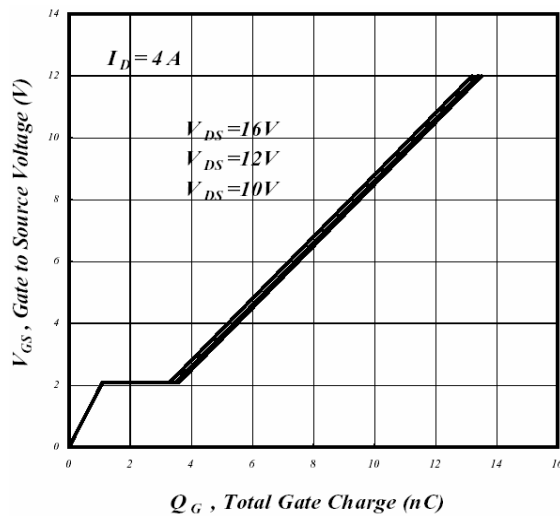


Fig 7. Gate Charge Characteristics

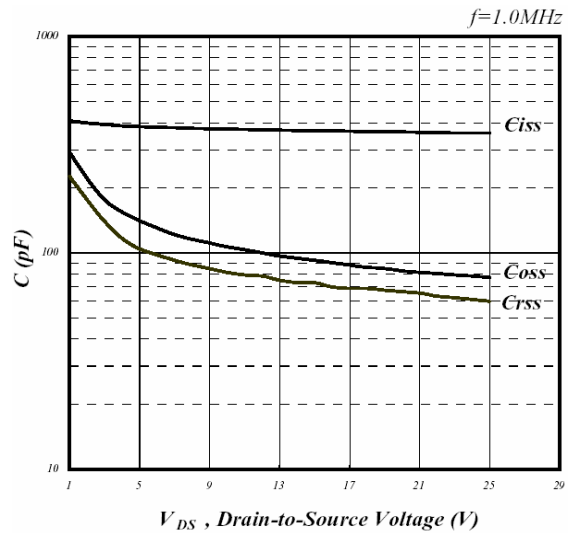


Fig 8. Typical Capacitance Characteristics

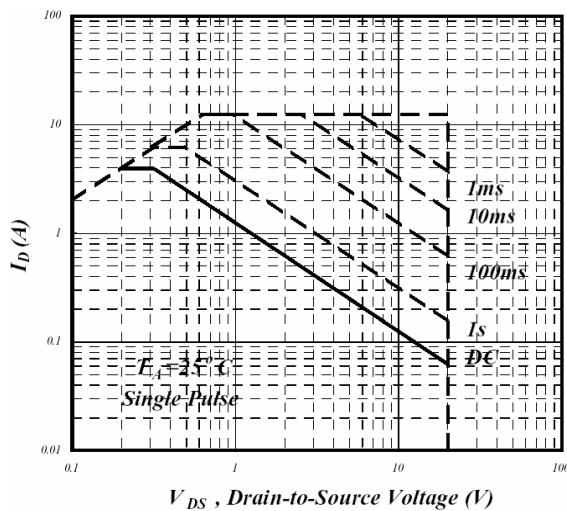


Fig 9. Maximum Safe Operating Area

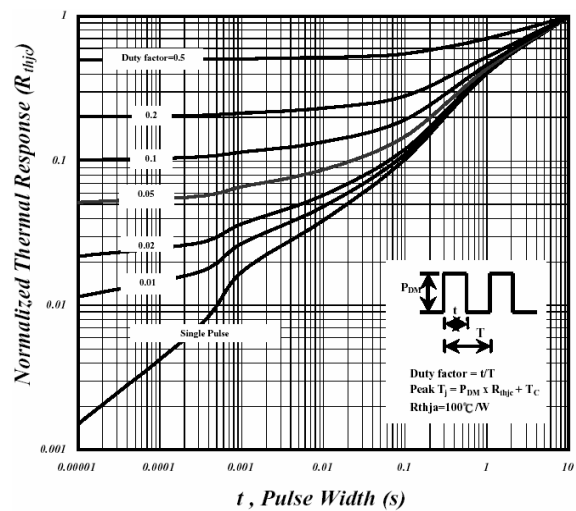


Fig 10. Effective Transient Thermal Impedance



Fig 11. Switching Time Waveform

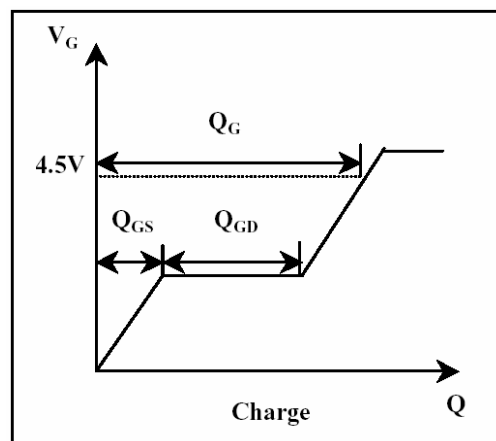


Fig 12. Gate Charge Waveform

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