# Appendix A - ATtiny261/461/861 Automotive specification at 150°C

This document contains information specific to devices operating at temperatures up to 150°C. Only deviations are covered in this appendix, all other information can be found in the complete Automotive datasheet. The complete Automotive datasheet can be found on www.atmel.com



8-bit **AVR**® Microcontroller with 2K/4K/8K Bytes In-System Programmable Flash

ATtiny261 ATtiny461 ATtiny861 Automotive

Appendix A

**Preliminary** 





### 1. Electrical Characteristics

### 1.1 Absolute Maximum Ratings<sup>(1)</sup>

Operating Temperature55°C to +150°C
Storage Temperature65°C to +175°C
Voltage on any Din eveent DECET
Voltage on any Pin except RESET with respect to Ground0.5V to V <sub>CC</sub> +0.5V
V.II. DEOET 'II. II. O. I. O. SVI. 40.0
Voltage on RESET with respect to Ground0.5V to +13.0V
M : 0 1: 1/ II
Maximum Operating Voltage 6.0\
DO 0 1 1/0 Di
DC Current per I/O Pin
DC Current V <sub>CC</sub> and GND Pins200.0 mA
DO Odiferit VCC and GIVD Filis200.0 III.

Note:

 Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or other conditions beyond those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

### 1.2 DC Characteristics

 $T_A = -40$ °C to 150°C,  $V_{CC} = 2.7$ V to 5.5V (unless otherwise noted)

Symbol	Parameter	Condition	Min.	Тур.	Max.	Unit
V <sub>IH1</sub>	Input High Voltage, XTAL1 pin	V <sub>CC</sub> = 2.7V to 5.5V	0.7V <sub>CC</sub> <sup>(2)</sup>		V <sub>CC</sub> + 0.5	V
I <sub>ACLK</sub>	Analog Comparator Input Leakage Current	$V_{CC} = 5V$ $V_{in} = V_{CC}/2$	-150		+150	nA

### 1.3 ADC Characteristics (Single-ended Mode)

Symbol	Parameter	Condition	Min	Тур	Max	Unit
DNL	Differential Non Linearity	V <sub>CC</sub> = 4V, V <sub>Ref</sub> = 4V, ADC clock = 200 kHz		0.3	1.0	LSB

# 1.4 ADC Characteristics (Differential Mode)<sup>(1)</sup> $T_A = -40^{\circ}\text{C}$ to 150°C, $V_{CC} = 2.7\text{V}$ to 5.5V (unless otherwise noted)

Symbol	Parameter	Condition	Min	Тур	Max	Unit
	- Resolution	Differential conversion, gain = 1x or 8x		8		Bit
	- Nesolulion	Differential conversion, gain = 20x or 32x		8		Bit
		Gain = 1x/8x, BIPOLAR, V <sub>CC</sub> = 5V, V <sub>Ref</sub> = 4V, ADC clock = 200 kHz		1.7	4.0	
<b>T</b> 1.15	Ab a data a saura su	Gain = 20x/32x, BIPOLAR, V <sub>CC</sub> = 5V, V <sub>Ref</sub> = 4V, ADC clock = 200 kHz		2.0	6.0	1.00
TUE	Absolute accuracy	Gain = $1x/8x$ , UNIPOLAR, $V_{CC} = 5V$ , $V_{Ref} = 4V$ , ADC clock = $200 \text{ kHz}$		2.3	6.0	LSB
		Gain = $20x/32x$ , UNIPOLAR, $V_{CC} = 5V$ , $V_{Ref} = 4V$ , ADC clock = $200 \text{ kHz}$		3.0	10.0	
	Integral Non Linearity	Gain = 1x/8x, BIPOLAR, V <sub>CC</sub> = 5V, V <sub>Ref</sub> = 4V, ADC clock = 200 kHz		0.3	1.5	
INII		Gain = $20x/32x$ , BIPOLAR, $V_{CC} = 5V$ , $V_{Ref} = 4V$ , ADC clock = $200 \text{ kHz}$		0.7	3.0	LOD
INL		Gain = $1x/8x$ , UNIPOLAR, $V_{CC} = 5V$ , $V_{Ref} = 4V$ , ADC clock = $200 \text{ kHz}$		1.0	3.0	- LSB
		Gain = $20x/32x$ , UNIPOLAR, $V_{CC} = 5V$ , $V_{Ref} = 4V$ , ADC clock = $200 \text{ kHz}$		2.0	8.0	
		Gain = $1x/8x$ , BIPOLAR, $V_{CC} = 5V$ , $V_{Ref} = 4V$ , ADC clock = $200 \text{ kHz}$		0.3	1.0	
DNII	Differential New Linearity	Gain = 20x/32x, BIPOLAR, V <sub>CC</sub> = 5V, V <sub>Ref</sub> = 4V, ADC clock = 200 kHz		0.4	1.2	
DNL	Differential Non Linearity	Gain = $1x/8x$ , UNIPOLAR, $V_{CC} = 5V$ , $V_{Ref} = 4V$ , ADC clock = $200 \text{ kHz}$		0.4	1.0	LSB
		Gain = $20x/32x$ , UNIPOLAR, $V_{CC} = 5V$ , $V_{Ref} = 4V$ , ADC clock = $200 \text{ kHz}$		0.8	2.5	

1. For temperature range +125°C to +150°C only. For -40°C to +125°C, refer to ATtiny261/461/861automotive datasheet. Note:





### 1.4 ADC Characteristics (Differential Mode)<sup>(1)</sup> (Continued)

 $T_A = -40$ °C to 150°C,  $V_{CC} = 2.7$ V to 5.5V (unless otherwise noted)

Symbol	Parameter	Condition	Min	Тур	Max	Unit	
	Gain error	Gain = $1x/8x$ , BIPOLAR, $V_{CC} = 5V$ , $V_{Ref} = 4V$ , ADC clock = $200 \text{ kHz}$	-4.0	+2.0	+4.0		
		Gain = $20x/32x$ , BIPOLAR, $V_{CC} = 5V$ , $V_{Ref} = 4V$ , ADC clock = $200 \text{ kHz}$	-4.0	+1.4	+4.0	LSB	
		Gain = $1x/8x$ , UNIPOLAR, $V_{CC} = 5V$ , $V_{Ref} = 4V$ , ADC clock = $200 \text{ kHz}$	-5.0	-2.6	+5.0	LSB	
		Gain = $20x/32x$ , UNIPOLAR, $V_{CC} = 5V$ , $V_{Ref} = 4V$ , ADC clock = $200 \text{ kHz}$	-5.0	-0.8	+5.0		
	Officet owner	BIPOLAR, V <sub>CC</sub> = 5V, V <sub>Ref</sub> = 4V, ADC clock = 200 kHz	-4.0		+4.0	LCD	
	Offset error	UNIPOLAR, V <sub>CC</sub> = 5V, V <sub>Ref</sub> = 4V, ADC clock = 200 kHz	-5.0		+5.0	LSB	
$V_{REF}$	Reference voltage		2.58		AVCC - 0.5	V	

Note: 1. For temperature range +125°C to +150°C only. For -40°C to +125°C, refer to ATtiny261/461/861automotive datasheet.

### 1.5 System and Reset Characteristics

Table 1-1. Reset, Brown-out and Internal Voltage Characteristics

Symbol	Parameter	Condition	Min	Тур	Max	Unit
$V_{BG}$	Bandgap reference voltage	V <sub>CC</sub> = 3V, T=150°C	0.99	1.1	1.21	V

**Table 1-2.** BODLEVEL Fuse Coding<sup>(1)</sup>

BODLEVEL [20] Fuses	Min V <sub>BOT</sub>	Typ V <sub>BOT</sub>	Max V <sub>BOT</sub>	Unit
111		BOD Disa	abled	
110	1.67	1.8	1.93	
101	2.5	2.7	2.9	V
100	3.98	4.3	4.62	
011				
010		Pagar	rod.	
001	Reserved			
000				

Note:

1. V<sub>BOT</sub> may be below nominal minimum operating voltage for some devices. For devices where this is the case, the device is tested down to V<sub>CC</sub> = V<sub>BOT</sub> during the production test. This guarantees that a Brown-out Reset will occur before V<sub>CC</sub> drops to a voltage where correct operation of the microcontroller is no longer guaranteed.

#### 1.6 Grade 0 Qualification

The ATtiny261/461/861 has been developed and manufactured according to the most stringent quality assurance requirements of ISO-TS-16949 and verified during product qualification as per AEC-Q100 grade 0.

AEC-Q100 qualification relies on temperature accelerated stress testing. High temperature field usage however may result in less significant stress test acceleration. In order to prevent the risk that ATtiny261/461/861 lifetime would not satisfy the application end-of-life reliability requirements, Atmel has extended the testing, whenever applicable (High Temperature Operating Life Test, High Temperature Storage Life, Data Retention, Thermal Cycles), far beyond the AEC-Q100 requirements. Thereby, Atmel verified the ATtiny261/461/861 has a long safe lifetime period after the grade 0 qualification acceptance limits.

The valid domain calculation depends on the activation energy of the potential failure mechanism that is considered. Therefore any temperature mission profile which could exceed the AEC-Q100 equivalence domain shall be submitted to Atmel for a thorough reliability analysis

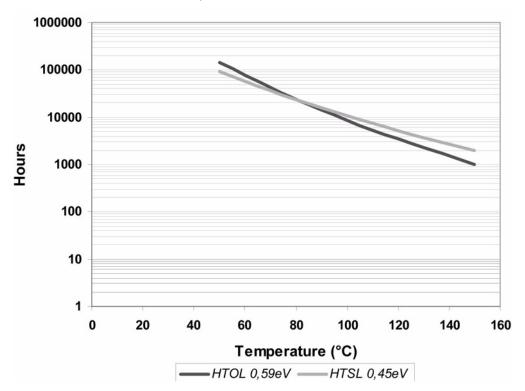


Figure 1-1. AEC-Q100 Lifetime Equivalence





#### **Ordering Information** 2.

#### ATtiny261/461/861 2.1

Speed (MHz)	Power Supply	Ordering Code	Package <sup>(1)</sup>	Operation Range
16 <sup>(2)</sup>	2.7 to 5.5V	ATtiny261-ESMD	PN	Extended (-40°C to +150°C)
16 <sup>(2)</sup>	2.7 to 5.5V	ATtiny261-ESXD	6G	Extended (-40°C to +150°C)
16 <sup>(2)</sup>	2.7 to 5.5V	ATtiny461-ESMD	PN	Extended (-40°C to +150°C)
16 <sup>(2)</sup>	2.7 to 5.5V	ATtiny461-ESXD	6G	Extended (-40°C to +150°C)
16 <sup>(2)</sup>	2.7 to 5.5V	ATtiny861-ESMD	PN	Extended (-40°C to +150°C)
16 <sup>(2)</sup>	2.7 to 5.5V	ATtiny861-ESXD	6G	Extended (-40°C to +150°C)

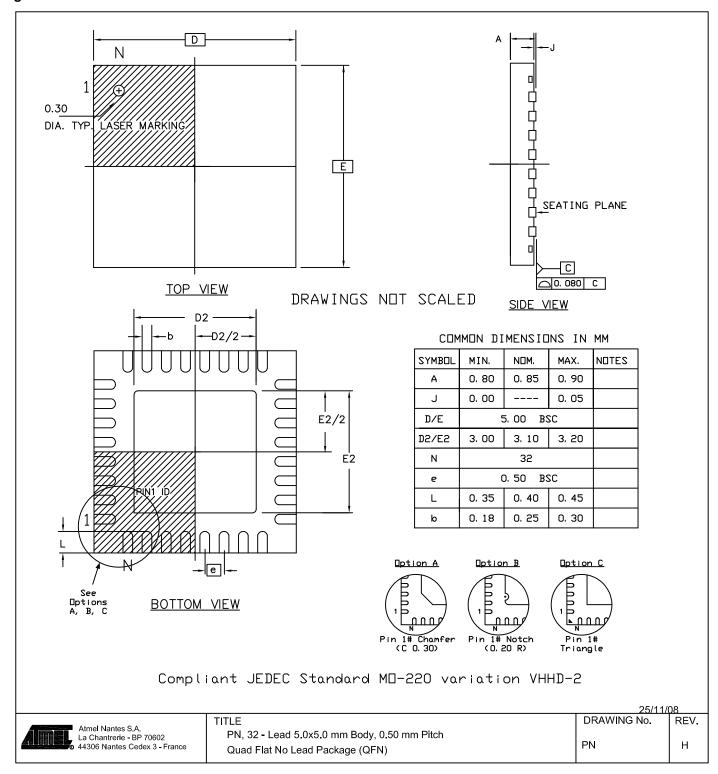
- Notes: 1. Pb-free packaging, complies to the European Directive for Restriction of Hazardous Substances (RoHS directive). Also Halide free and fully Green.
  - 2. For Speed versus  $\ensuremath{V_{\text{cc}}},$  see complete datasheet.

	Package Type						
	PN	32-pad, 5 x 5 x 1.0 mm body, lead pitch 0.50 mm, Quad Flat No-Lead/Micro Lead Frame Package (QFN/MLF): E2/D2 3.1 +/-0.1mm					
(	6G	20-leads, 4.4x6.5mm body - 0.65mmPitch - Lead Length: 0.6mm Thin Shrink Small Outline Package (TSSOP)					

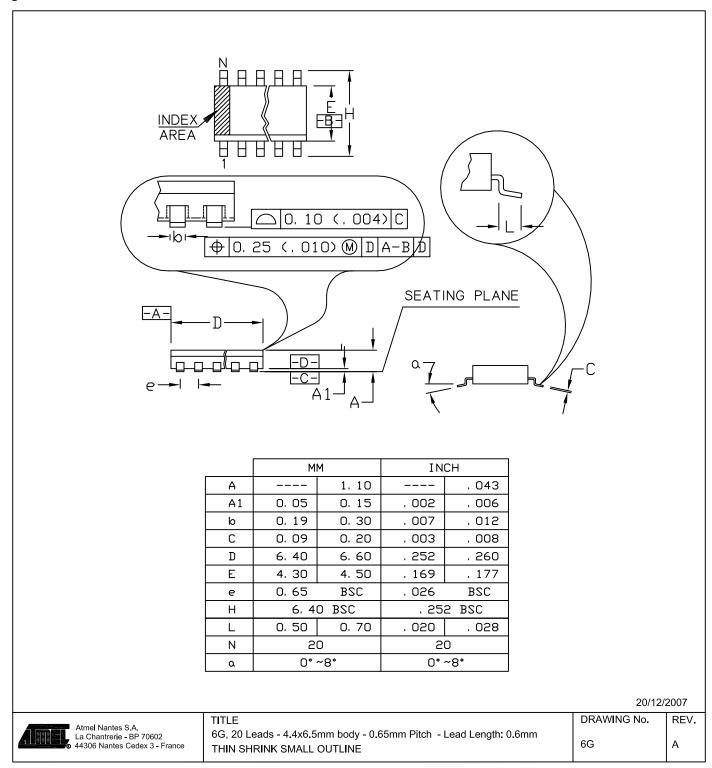




Figure 2-1. PN



**Figure 2-2.** 6G







## 3. Document Revision History

- 3.1 7793C 06/10
- 1. DC characteristics updated.
- 2. ADC characteristics updated.
- 3. RC Oscillator removed.
- 3.2 7793B 03/10
- 1. DC characteristics updated.
- 2. ADC characteristics updated.
- 3.3 7793A 08/08
- 1. Document Creation.



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